

A Review of Design, Development, Control and Applications of DC–DC Converters

Vinod Meena¹, Chirag Gupta²,

^{1,2}Department of Electrical Engineering,

¹RKDF University, Bhopal, India

Abstract: The DC–DC converters have become research interest due to high utilization in various applications. The DC–DC converter is required to change DC electrical energy from one voltage level to another voltage level efficiently. A DC–DC converters are widely used in DC motor drive applications and in regulated switched mode DC power supplies. This paper presents a comprehensive review on various types of DC–DC converters with their recent progress, development and features. In this age of modern electronics, the smart devices, gadgets, solar PV system and Internet of things (IoT) require appropriate DC level voltage conversion, control and regulation. This review is concluded with future aspects and applications with reference to DC–DC converters.

Keywords: DC–DC converter, SMPS, Classification, Voltage Regulation.

I INTRODUCTION

Modern power supply system represent the conversation of different level of voltages for every circuit designer across the electrical designs. For smart sensors, Internet of Things (IoT), transportation, medical, automation, gadgets and fitness circuit solutions, one of the elementary challenging task is to design an appropriate power-supply circuit network. Power-supply is categorized into many levels, at the top level are the basic AC–AC, AC–DC, DC–AC and DC–DC converters. Modern electronic devices require DC–DC conversion in terms of shifting the power signal down with a low-dropout regulator (LDO) or buck topologies, and up with a boost topology.

Nowadays, the Switched-Mode Power Supply (SMPS) [1] plays an important role in the consumer power market. With its small size, it can be found everywhere from mobile phone chargers to Plasma TVs. It is used to convert the electricity from one voltage/current to the other voltage/current. For a simple example, a mobile phone charger converts the electricity from the plug with around 220 V AC to 9 V DC to supply the mobile phone [2].

Typically, the SMPS solution consists of several electronic components: a switching controller, a power MOSFET, a transformer, diodes, resistors, capacitors and inductors. In order to obtain the complete SMPS design, a long list of calculations has to be done in order to figure out the appropriate

value for all electronic components [3].

1.1 Switched-Mode Power Supply (SMPS)

This can hardly ideate the lifestyle without the provision and processing activities which use electrical energy, and its supply. A host of devices in everyday use are operated either directly from the mains power grid, from the vehicle power supply in an automobile or using accumulators. The electronic circuits in modern devices in entertainment, data processing or industrial electronics are mostly supplied with direct voltages from 12 V down to below 1 V. To be able to operate these devices from the common alternating voltage mains network, or to change up the internal accumulators, a power supply is required [4, 5].

Conventional power supply consists of a mains transformer for voltage reduction and galvanic isolation from the mains, a rectifier for producing a direct voltage and a bulk capacitor for voltage smoothing.

1.2 Architecture of The Switched-Mode Power Supplies

Switched-mode power supplies (SMPS) are power supplies which chop the rectified and filtered mains voltage at a frequency which is significantly higher than the 50 Hz of the mains alternating current as shown in Figure 1.

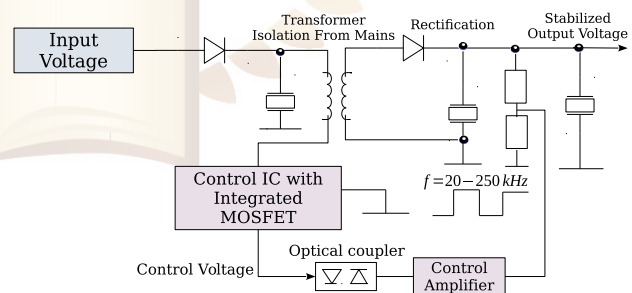


Figure 1: Switched-Mode Power Supply

By using the semiconductor switches exclusively only switching and forward losses arise. This is responsible for the characteristically high efficiency of a pulsed power supply by comparison with analog methods. Regulation is effected either by altering the pulse duty factor at a constant frequency, or by changing the frequency for a fixed or variable

pulse duty factor [6]. The voltage chopped in this way can be transformed to any other required voltage, and rectified. The ferrite core transformer serves not only to make the required voltage conversion and provide galvanic isolation from the mains supply but also, depending on its working principle, to store the magnetic energy [7].

With SMPS, the pulsing results in harmonic waves, which can cause radiated interference to radio and TV reception, as well as to communication transmissions. Legislation demands a limit on the interference signal levels for all electrical devices and systems which produce high frequency energy [8].

An SMPS which is powered from the alternating voltage mains and which supplies a DC voltage at its output is also called an AC–DC converter. If a direct current source (e.g. an AC–DC converter, or a battery) is connected to the input, then we speak of a DC–DC converter, or a switching regulator [9]. If the voltage is not rectified at the output, the device is a DC-AC converter or inverter. If the SMPS is supplied from the mains, and there is no rectification on the output side, we have an AC–AC converter, i.e. an alternating current converter [10].

1.3 Operation Principle and Circuit System of Switching Power Supply

As mentioned above, stabilization mode of power supply is roughly classified into switching mode and series mode. Nowadays, power supply means switching system in many cases due to high efficiency and compact [11]. Here, the mechanism of switching power supply is explained.

1.3.1 Operation Principle

Basic circuit and components of switching power supply is shown in Figure 2.

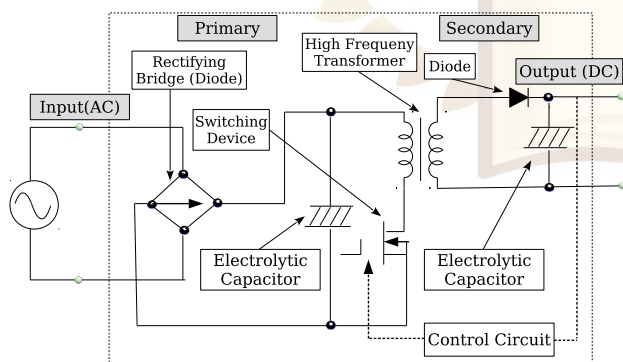


Figure 2: Basic Circuit and Components of Switching Power Supply

- Rectifying bridge: To rectify AC current to one direction.

- Electrolytic capacitor: To build up electricity and work to keep voltage.
- High frequency transformer: To transfer energy from primary to secondary.
- Control circuit: To control ON/OFF timing of switching device to stabilize secondary voltage.

In this system, input (alternate current: AC) is converted to output (direct current: DC). Input side is called “Primary” output side is called “Secondary” to which energy is transferred via high frequency transformer.

Now, referring to the diagram above, operation mechanism of switching power supply can be explained as follows,

1. Connect alternate current (AC) to switching power supply.
2. The AC is rectified by rectifying bridge and smoothed by primary electrolytic capacitor after that.
3. Switching operation (repeated electric ON/OFF operation) of switching device generates alternate current with high frequency.
4. Energy (AC) is transferred via high frequency transformer to secondary side.
5. Rectified by secondary diode and smoothed by secondary electrolytic capacitor, the energy is converted to DC (direct current) as output.
6. To keep output voltage stabilized, switching is controlled through feedback system.

That is the basic operation principle of switching power supply.

II BACKGROUND AND REVIEW

There are various methods in switching power supply depends on DC–DC converter’s mode which converts DC to AC with high frequency, and again converts it back to DC [12]. Also, in determining switching cycle of DC–DC converter, it is classified into two modes. One is called self-excitation mode whose switching block determines the switching cycle on its own [13]. The other method is called separate excitation mode (PWM mode) that has an oscillator to decide the frequency independently. Self-excitation mode’s features are “Cost is low due to simple circuit structure,” and “the frequency changes according to input voltage and load condition” [14, 15].

Separate excitation mode’s features are “Cost is generally high compared to self-excitation mode as it uses ICs”, and “the frequency is constant”. Also, there are another two modes when energy is transferred from primary to secondary [16]. One is called forward mode where the energy is transferred during ON period, and the other is called fly back mode where the energy is transferred during OFF period [17][18].

2.0.1 Single Forward DC–DC Converter

This mode is used in many switching power supplies due to simple structure and stable control as presented in Figure 3. (Adopted in our Nonstop power supplies in many cases). Separate excitation mode is mostly used from small power to high power as well. Disadvantage is poor usability of transformer [19, 20].

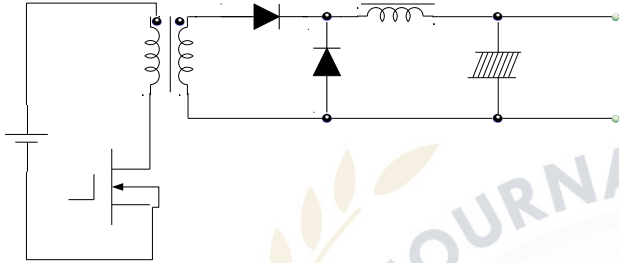


Figure 3: Single Forward DC–DC Converter

2.0.2 Flyback DC–DC Converter (RCC)

This mode need a few components and is the simplest mode, but not suitable to high power. This is mostly adopted to small power, but input voltage range is wide as presented in Figure 4 [21, 22].

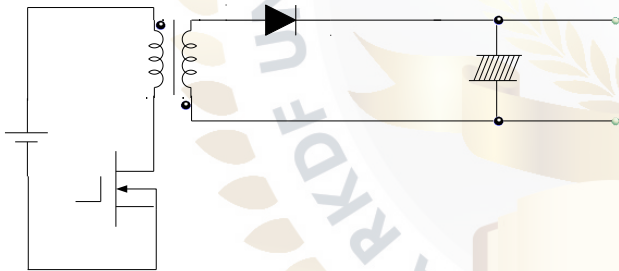


Figure 4: Flyback DC–DC Converter

2.0.3 Push-Pull DC–DC Converter

This mode uses two switching devices and coils to turn on alternately. Bias magnetism of transformer is critical as presented in Figure 5 [23, 24].

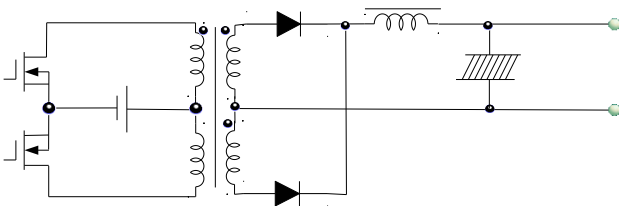


Figure 5: Push-Pull DC–DC Converter

2.0.4 Half-Bridge DC–DC Converter

Operation is the same as push-pull mode, but as the applied to transform is half of V_i , low voltage transistors can be used [25]. The usability of transformer is better, but the temperature rise of each capacitor caused by switching current that flows in capacitors is critical as presented in Figure 6 [26, 27].

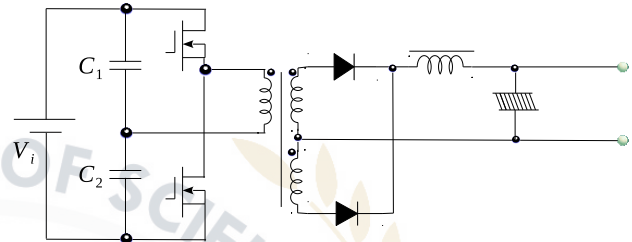


Figure 6: Half-Bridge DC–DC Converter

2.0.5 Full-Bridge DC–DC Converter

Circuit structure is complicated, but low voltage switching devices can be used as presented in Figure 7. It gives high efficiency and is also improve the tracking capability of the PV, soft computing technique is used to utilize the PV power effectively. Here, DPSO is used to maximize the PV power. In this paper Performance of SEPIC converter compared with buck-boost converter under both normal and partial shading conditions of PV system. This paper gives the new intelligent control technique in order to track the global maxima of PV power effectively and also gives MATLAB-Simulink to control the system parameters via DPSO method. opted to high power [28]. Usability of transformer is the highest of all. Critical points are bias magnetism and penetration current between upper and lower devices (FETs) [29, 30].

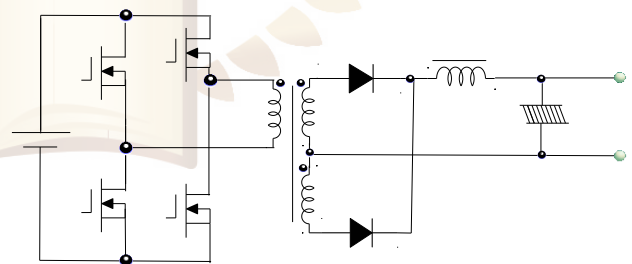


Figure 7: Full-Bridge DC–DC Converter

III APPLICATIONS AND CLASSIFICATIONS

The main applications fields for SMPS are:

- Consumer electronics: TVs, DVD players, video recorders, set top boxes, satellite receivers, chargers and external power supply units.
- Electronic DP: PCs, servers, monitors, notebooks.
- Telecommunications: mobile communication base stations, switching stations, mobile phone chargers.
- Industrial electronics: open and closed loop control engineering, measuring instruments, auxiliary power supplies, battery chargers etc.

3.1 Advantages and Disadvantages

The main advantage of this method is greater efficiency because the switching MOSFET dissipates little power when it is outside of its active region (i.e., when the MOSFET acts like a switch and either has a negligible voltage drop across it or a negligible current through it). Other advantages include smaller size and lighter weight (from the elimination of low frequency transformers which have a high weight) and lower heat generation due to higher efficiency.

Disadvantages include greater complexity, the generation of high-amplitude, high-frequency energy that the low-pass filter must block to avoid electromagnetic interference (EMI), and a ripple voltage at the switching frequency and the harmonic frequencies thereof. Very low cost SMPS may couple electrical switching noise back onto the mains power line, causing interference with A/V equipment connected to the same phase. Non power-factor-corrected SMPS also cause harmonic distortion.

3.2 Classification of SMPS

Switched-mode power supplies can be classified according to the circuit topology. The most important distinction is between isolated converters and non-isolated ones.

3.2.1 Non-Isolated Topologies

Non-isolated converters are simplest, with the three basic types using a single inverter for energy storage [31]. In the Voltage relation column, D is the duty cycle of the converter, and can vary from 0 to 1. V_{in} is assumed to be greater than zero; if it is negative, negate V_{out} to match. The list of Non-isolated topology is listed below in Table 1 [32].

Table 1: Non-Isolated Topologies

Type	Power [Watts]	Typical Efficiency	Relative Cost	Energy Storage	Voltage Relation	Features
Buck	0-1000	75%	1.0	Single Inductor	$0 \leq Out \leq In,$ $Out = In \times D$	Continuous Output
Boost	0-150	78%	1.0	Single Inductor	$Out \geq In,$ $Out = \frac{In}{(1-D)}$	Continuous Input
Buck-Boost	0-150	78%	1.0	Single Inductor	$Out \leq 0,$ $Out = -In \times \frac{D}{(1-D)}$	Inverted Output Voltage

The buck, boost, and buck-boost topologies are all strongly related. Input, output and ground come together at one point. One of the three passes through an inductor on the way, while the other two pass through switches. One of the two switches must be active (e.g. a MOSFET), while the other can be a diode [33][34]. Sometimes, the topology can be changed simply by re-labeling the connections. A 12 V input, 5 V output buck converter can be converted to a 7 V input, -5 V output buck-boost by grounding the “output” and taken the output from the “ground” pin. Switching becomes less efficient as duty cycles become extremely short. For large voltage changes, a transformer (isolated) topology may be better [35, 36].

3.2.2 Isolated Topologies

All isolated topologies include a transformer, and thus can produce an output of higher or lower voltage than the input by adjusting the turn’s ratio [37]. For some topologies, multiple windings can be placed on the transformer to produce multiple output voltages. Some converters use the transformer for energy storage, while others use a separate inductor [38].

Table 2: Isolated Topologies

Type	Power [Watts]	Typical Efficiency	Relative Cost	Input Range	Energy Storage	Features
Fly-back	0-250	78%	1.0	5-600 V	Transformer	Isolated form of Boost converter
Half-Forward	0-250	75%	1.2	5-500 V	Inductor	
Forward		78%		60-200 V	Inductor	Isolated form of Buck converter
Push-Pull	100-1000	72%	1.75	50-1000 V	Inductor	
Half-Bridge	0-2000	72%	1.9	50-1000 V	Inductor	
Full-Bridge	400-5000	69%	> 2.0	50-1000 V	Inductor	Very efficient use of transformer

IV PROPOSED METHODOLOGY

Modern electronics, the smart devices, gadgets, solar PV system and Internet of things (IoT) require appropriate DC level voltage conversion, control and regulation. For improving the conversion and control efficiency, soft computing techniques like fuzzy logic, neural network, genetic algorithm, artificial intelligence techniques etc. are required to utilize effectively. These methods can be used to maximize the solar PV power. Soft computing establish the new intelligent control technique in order to regulate smart electronic devices and maximize the PV system power effectively.

V CONCLUSION

Soft computing based DC–DC converter for discontinuous and continuous conduction mode significantly improve the conversion process. The unique features is the comparison of time response specification among two controllers for different input voltage. The linearized model of DC–DC converter is obtained by using state space averaging technique. The values of time domain specifications have improved more in fuzzy logic controller rather in PI controller.

REFERENCES

- [1] B. W. Williams, *Switched-mode Power Supplies*. London: Macmillan Education UK, 1987, pp. 309–329. [Online]. Available: https://doi.org/10.1007/978-1-349-18525-2_15
- [2] S. Saab, A. Leiserson, and M. Tunstall, “Key extraction from the primary side of a switched-mode power supply,” in *2016 IEEE Asian Hardware-Oriented Security and Trust (AsianHOST)*, Dec 2016, pp. 1–7. [Online]. Available: <https://doi.org/10.1109/AsianHOST.2016.7835563>
- [3] H. Gumhalter, *Switching Mode Power Supplies*. Berlin, Heidelberg: Springer Berlin Heidelberg, 1995, pp. 176–225. [Online]. Available: https://doi.org/10.1007/978-3-642-78403-3_9
- [4] S. Singh, B. Singh, G. Bhuvaneswari, and V. Bist, “Improved power quality switched-mode power supply using buck boost converter,” *IEEE Transactions on Industry Applications*, vol. 52, no. 6, pp. 5194–5202, Nov 2016. [Online]. Available: <https://doi.org/10.1109/TIA.2016.2600675>
- [5] S. Singh, B. Singh, G. Bhuvaneswari, and Bist, “Power corrected bridgeless converter based switched mode power supply factor,” *IET Power Electronics*, vol. 9, no. 8, pp. 1684–1693, 2016. [Online]. Available: <https://doi.org/10.1049/iet-pel.2014.0933>
- [6] S. Singh, G. Bhuvaneswari, and B. Singh, “A power factor corrected two stage switched mode power supply,” in *2012 IEEE International Conference on Power Electronics, Drives and Energy Systems (PEDES)*, Dec 2012, pp. 1–6. [Online]. Available: <https://doi.org/10.1109/PEDES.2012.6484480>
- [7] S. Singh, G. Bhuvaneswari, and Singh, “Analysis and design of bridgeless switched mode power supply for computers,” *Journal of The Institution of Engineers (India): Series B*, vol. 95, no. 3, pp. 175–184, Sep 2014. [Online]. Available: <https://doi.org/10.1007/s40031-014-0096-x>
- [8] S. Singh, G. Bhuvaneswari, and Singh, “Improved power quality smps for personal computer applications,” *Journal of The Institution of Engineers (India): Series B*, vol. 93, no. 3, pp. 151–161, Sep 2012. [Online]. Available: <https://doi.org/10.1007/s40031-012-0023-y>
- [9] A. Parayandeh and A. Prodic, “Programmable analog-to-digital converter for low-power dc-dc smps,” *IEEE Transactions on Power Electronics*, vol. 23, no. 1, pp. 500–505, Jan 2008. [Online]. Available: <https://doi.org/10.1109/TPEL.2007.913932>
- [10] N. Patin, “1 - non-isolated switch-mode power supplies,” in *Power Electronics Applied to Industrial Systems and Transports, Volume 3*, N. Patin, Ed. Elsevier, 2015, pp. 1 – 18. [Online]. Available: <https://www.sciencedirect.com/science/article/pii/B9781785480027500016>
- [11] R. Kalpana, B. Singh, and G. Bhuvaneswari, “An improved power quality converter for three-phase switched mode power supplies,” in *Electrical, Electronics and Computer Science (SCEECS), 2014 IEEE Students’ Conference on*, March 2014, pp. 1–5. [Online]. Available: <https://doi.org/10.1109/SCEECS.2014.6804473>
- [12] J. Hannonen, J. Honkanen, J. P. Ström, T. Kärkkäinen, S. Räisänen, and P. Silventoinen, “Capacitor aging detection in a dc-dc converter output stage,” *IEEE Transactions on Industry Applications*, vol. 52, no. 4, pp. 3224–3233, July 2016. [Online]. Available: <https://doi.org/10.1109/TIA.2016.2550527>
- [13] C. M. Wang, C. H. Lin, and T. C. Yang, “High-power-factor soft-switched dc power supply system,” *IEEE Transactions on Power Electronics*, vol. 26, no. 2, pp. 647–654, Feb 2011. [Online]. Available: <https://doi.org/10.1109/TPEL.2010.2064336>
- [14] Y.-x. Gao, S.-b. Guo, X.-f. Lin-Shi, and B. Allard, “Design of low-power high-frequency digital controlled dc-dc switching power converter,” *Journal of Shanghai University (English Edition)*, vol. 12, no. 5, pp. 450–456, Oct 2008. [Online]. Available: <https://doi.org/10.1007/s11741-008-0514-2>
- [15] A. Radić, Z. Lukić, A. Prodić, and R. H. de Nie, “Minimum-deviation digital controller ic for dc-dc switch-mode power supplies,” *IEEE Transactions on Power Electronics*, vol. 28, no. 9, pp. 4281–4298, Sept 2013. [Online]. Available: <https://doi.org/10.1109/TPEL.2012.2227503>
- [16] F. Alonge, M. Pucci, R. Rabbeni, and G. Vitale, “Dynamic modelling of a quadratic dc/dc single-switch boost converter,” *Electric Power Systems Research*, vol. 152, no. Supplement C, pp. 130 – 139, 2017. [Online]. Available: <http://www.sciencedirect.com/science/article/pii/S0378779617302845>
- [17] X. Hu and Y. Liu, *Structure Analysis on High Frequency Conversion Circuit of Switching Power Supply*. Berlin, Heidelberg: Springer Berlin Heidelberg, 2013, pp. 181–186. [Online]. Available: https://doi.org/10.1007/978-3-642-31528-2_30
- [18] H. Li, B. Zhang, P. Luo, and Z. Li, “Adaptive duty ratio modulation technique in switching dc–dc converter operating in discontinuous conduction mode,” *Analog Integrated Circuits and Signal Processing*, vol. 78, no. 2, pp. 361–371, Feb 2014. [Online]. Available: <https://doi.org/10.1007/s10470-013-0231-7>

- [19] Y. Wang, J. Wang, L. Di, Y. Liu, Y. Wang, and K. Wang, *Modeling and Stability Investigation of Single-Ended Forward DC/DC Converter*. Berlin, Heidelberg: Springer Berlin Heidelberg, 2011, pp. 308–313. [Online]. Available: https://doi.org/10.1007/978-3-642-21411-0_50
- [20] B. Singh and G. D. Chaturvedi, “Analysis, design and development of single switch forward buck ac–dc converter for low power battery charging application,” in *2006 International Conference on Power Electronic, Drives and Energy Systems*, Dec 2006, pp. 1–6. [Online]. Available: <https://doi.org/10.1109/PEDES.2006.344285>
- [21] Y. F. Chen, T. D. Nguyen, J. Y. Lin, Y. C. Hsieh, and H. J. Chiu, “Hybrid-switching asymmetrical half-bridge flyback dc-dc converter,” in *2016 IEEE International Conference on Industrial Technology (ICIT)*, March 2016, pp. 1313–1317. [Online]. Available: <https://doi.org/10.1109/ICIT.2016.7474945>
- [22] R. Lonappan and D. David, “Flyback dc-dc converter for full load range applications,” in *2015 International Conference on Electrical, Electronics, Signals, Communication and Optimization (EESCO)*, Jan 2015, pp. 1–5. [Online]. Available: <https://doi.org/10.1109/EESCO.2015.7253979>
- [23] Y. V. Hote, D. R. Choudhury, and J. R. P. Gupta, “Robust stability analysis of the pwm push-pull dc–dc converter,” *IEEE Transactions on Power Electronics*, vol. 24, no. 10, pp. 2353–2356, Oct 2009. [Online]. Available: <https://doi.org/10.1109/TPEL.2009.2014132>
- [24] D. Czarkowski, L. R. Pujara, and M. K. Kazimierzczuk, “Robust stability of state-feedback control of pwm dc–dc push-pull converter,” *IEEE Transactions on Industrial Electronics*, vol. 42, no. 1, pp. 108–111, Feb 1995. [Online]. Available: <https://doi.org/10.1109/41.345854>
- [25] C. j. Wu, F. c. Lee, S. Balachandran, and H. L. Goin, “Design optimization for a half-bridge dc–dc converter,” *IEEE Transactions on Aerospace and Electronic Systems*, vol. AES-18, no. 4, pp. 497–508, July 1982. [Online]. Available: <https://doi.org/10.1109/TAES.1982.309256>
- [26] C. R. Sullivan and S. R. Sanders, “Soft-switched square-wave half-bridge dc–dc converter,” *IEEE Transactions on Aerospace and Electronic Systems*, vol. 33, no. 2, pp. 456–463, April 1997. [Online]. Available: <https://doi.org/10.1109/7.575881>
- [27] H. Mao, J. Abu-Qahouq, S. Luo, and I. Batarseh, “Zero-voltage-switching half-bridge dc–dc converter with modified pwm control method,” *IEEE Transactions on Power Electronics*, vol. 19, no. 4, pp. 947–958, July 2004. [Online]. Available: <https://doi.org/10.1109/TPEL.2004.830052>
- [28] A. Diker, D. Korkmaz, Ö. F. Alçın, U. Budak, and M. Gedikpinar, *Design and Implementation of A Single-Stage Full-Bridge DC/DC Converter with ZVS Mode*. Cham: Springer International Publishing, 2014, pp. 347–353. [Online]. Available: https://doi.org/10.1007/978-3-319-02294-9_44
- [29] H.-L. Do, *Full-Bridge High Step-Up DC–DC Converter with Two Stage Voltage Doubler*. Berlin, Heidelberg: Springer Berlin Heidelberg, 2012, pp. 251–254. [Online]. Available: https://doi.org/10.1007/978-3-642-27287-5_40
- [30] M. Borage, S. Tiwari, S. Bhardwaj, and S. Kotaiah, “A full-bridge dc–dc converter with zero-voltage-switching over the entire conversion range,” *IEEE Transactions on Power Electronics*, vol. 23, no. 4, pp. 1743–1750, July 2008. [Online]. Available: <https://doi.org/10.1109/TPEL.2008.925203>
- [31] C. Anandababu and B. G. Fernandes, “Leakage current generation in view of circuit topology of non-isolated full-bridge neutral point clamped grid-tied photovoltaic inverters,” *IET Power Electronics*, vol. 9, no. 8, pp. 1571–1580, 2016. [Online]. Available: <https://doi.org/10.1049/iet-pel.2015.0802>
- [32] S. Ye, W. Eberle, and Y. F. Liu, “A novel non-isolated full bridge topology for vrm applications,” *IEEE Transactions on Power Electronics*, vol. 23, no. 1, pp. 427–437, Jan 2008. [Online]. Available: <https://doi.org/10.1109/TPEL.2007.911848>
- [33] B. Moon, H. Y. Jung, S. H. Kim, and S. H. Lee, “A modified topology of two-switch buck-boost converter,” *IEEE Access*, vol. 5, pp. 17772–17780, 2017. [Online]. Available: <https://doi.org/10.1109/ACCESS.2017.2749418>
- [34] M. Muhammad, M. Armstrong, and M. A. Elgendy, “Analysis and implementation of high-gain non-isolated dc–dc boost converter,” *IET Power Electronics*, vol. 10, no. 11, pp. 1241–1249, 2017. [Online]. Available: <https://doi.org/10.1049/iet-pel.2016.0810>
- [35] L. Cong and H. Lee, “High-voltage high-frequency non-isolated dc–dc converters with passive-saving two-phase qsw-zvs technique,” *Analog Integrated Circuits and Signal Processing*, vol. 88, no. 2, pp. 303–317, Aug 2016. [Online]. Available: <https://doi.org/10.1007/s10470-016-0762-9>
- [36] B. S. Revathi and M. Prabhakar, “Non isolated high gain dc-dc converter topologies for pv applications - a comprehensive review,” *Renewable and Sustainable Energy Reviews*, vol. 66, no. Supplement C, pp. 920 – 933, 2016. [Online]. Available: <http://www.sciencedirect.com/science/article/pii/S1364032116304907>
- [37] H. Chen, A. Prasai, and D. Divan, “A modular isolated topology for instantaneous reactive power compensation,” *IEEE Transactions on Power Electronics*, vol. PP, no. 99, pp. 1–1, 2017. [Online]. Available: <https://doi.org/10.1109/TPEL.2017.2688393>
- [38] G. G. Oggier and M. Ordonez, “Boundary control for isolated topologies: The natural switching surface for full-bridge zvs,” in *2012 IEEE Energy Conversion Congress and Exposition (ECCE)*, Sept 2012, pp. 3981–3987. [Online]. Available: <https://doi.org/10.1109/ECCE.2012.6342159>